IN THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input;

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said first parallel communications channels are provided over at least one electrical conductor.

2. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input; and

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said LLD receive interface further includes at least one LLD status output;

said PLD send interface further includes at least one PLD status input; and

further comprising at least one third communications channel connecting said at least one LLD status output to said at least one PLD status input.

3. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input; and

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said PLD further comprises a PLD receive interface including PLD parallel information inputs and at least one PLD control input; and

said LLD further comprises an LLD send interface including LLD parallel information outputs and at least one LLD control output; and

further comprising third communications channels connecting said LLD information outputs to respective PLD information inputs, and at least one fourth communications channel connecting said at least one LLD control output with said at least one PLD control input so that said PLD and LLD are operable in a push-push configuration.

4. (previously presented) A communications system according to Claim 3 wherein:

said PLD send interface and said LLD send interface are substantially identical; and

wherein said PLD receive interface and said LLD receive interface are mirrored to thereby define symmetrical interfaces.

5. (previously presented) A communications system according to Claim 3 wherein:

said PLD receive interface further includes at least one PLD status output; and

wherein said LLD send interface further includes at least one LLD status input; and

further comprising at least one fifth communications channel connecting said at least one PLD status output to said at least one LLD status input.

6. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input; and

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said LLD comprises an asynchronous transfer mode (ATM) device.

7. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input; and

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said PLD comprises one of a synchronous optical network (SONET) device and a synchronous digital hierarchy (SDH) device.

8. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input; and

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs, and at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said PLD send interface comprises a string-based framing coder for determining and appending a string-based framing code to each information symbol string of information symbol strings to be transmitted in parallel over respective first parallel communications channels, each string-base framing code being based upon at least some of the information symbols in the respective information symbol string; and

said LLD receive interface comprises a deskewer for aligning received parallel information symbol strings based upon the string-based framing codes.

9. (previously presented) A communications system according to Claim 8 wherein:

each information symbol comprises a binary bit; and

wherein said string-based coder comprises a cyclic redundancy checking (CRC) coder for determining and appending CRC codes to respective information bit strings.

10. (previously presented) A communications system according to Claim 9 wherein:

said deskewer comprises a CRC framer for framing said information bit strings based upon said CRC codes.

11. (previously presented) A communications system according to Claim 8 wherein said deskewer comprises:

a framer for framing information symbol strings based upon said respective string-based framing codes; and

an aligner for aligning framed information symbol strings relative to one another and based upon said string-based framing codes.

12. (previously presented) A communications system according to Claim 11 wherein:

each information symbol comprises a binary bit; and

wherein said aligner comprises:

at least one first-in-first-out (FIFO) device connected to said framer for buffering framed information bit strings; and

a FIFO controller for aligning framed information bit strings during at least one of a writing and a reading phase of said at least one FIFO device and based upon said string-based framing codes.

13. (canceled)

14. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs, at least one PLD control output, and at least one PLD status input, a PLD receive interface including PLD parallel information inputs, at least one PLD control input, and at least one PLD status output;

a logical link layer device (LLD) comprising an LLD receive interface including LLD parallel information inputs, at least one LLD control input, at least one LLD status output, an LLD send interface including LLD parallel information outputs, at least one LLD control output, and at least one LLD status input;

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs;

at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input;

at least one third communications channel connected said at least one LLD status output to said at least one PLD status input;

fourth parallel communications channels connecting said LLD information outputs to respective PLD information inputs;

at least one fifth communications channel connecting said at least one LLD control output to said at least one PLD control input; and

at least one sixth communications channel connected said at least one PLD status output to said at least one LLD status input.

15. (previously presented) A communications system according to Claim 14 wherein:

said PLD send interface and said LLD send interface are mirrored; and

wherein said PLD receive interface and said LLD receive interface are mirrored to thereby define symmetrical interfaces.

16. (previously presented) A communications system according to Claim 14 wherein said LLD comprises:

an asynchronous transfer mode (ATM) device.

17. (previously presented) A communications system according to Claim 14 wherein said PLD comprises:

one of a synchronous optical network (SONET) device and a synchronous digital hierarchy (SDH) device.

18. (previously presented) A communications system according to Claim 14 wherein:

said PLD send interface comprises a string-based framing coder for determining and appending a string-based framing code to each information symbol string of information symbol strings to be transmitted in parallel over respective first parallel communications channels, each string-based framing code being based upon at least some of the information symbols in said respective information symbol string; and

wherein said LLD receive interface comprises a deskewer for aligning received parallel information symbol strings based upon said string-based framing codes.

19. (previously presented) A communications system according to Claim 18 wherein:

each information symbol comprises a binary bit; and

said string-based coder comprises a cyclic redundancy checking (CRC) coder for determining and appending CRC codes to respective information bit strings.

20. (previously presented) A communications system according to Claim 19 wherein said deskewer comprises:

a CRC framer for framing said information bit strings based upon said CRC codes.

21. (previously presented) A communications system comprising:

a physical layer device (PLD) comprising a PLD send interface including PLD parallel information outputs and at least one PLD control output;

a logical link layer device (LLD) comprising an LLD receive interface including LLD parallel information inputs and at least one LLD control input;

first parallel communications channels connecting said PLD information outputs to respective LLD information inputs;

at least one second communications channel connecting said at least one PLD control output to said at least one LLD control input;

said PLD send interface further comprising a string-based framing coder for determining and appending a string-based framing code to each information symbol string of information symbol strings to be transmitted in parallel over respective first parallel communications channels, each string-based framing code being based upon at least some of said information symbols in said respective information symbol string;

said LLD receive interface further comprising a deskewer for aligning received parallel information symbol strings based upon said string-based framing codes.

22. (previously presented) A communications system according to Claim 21 wherein:

said PLD send interface and said LLD send interface are substantially identical; and

said PLD receive interface and said LLD receive interface are mirrored to thereby define symmetrical interfaces.

23. (previously presented) A communications system according to Claim 21 wherein:

said LLD receive interface further includes at least one LLD status output; and

said PLD send interface further includes at least one PLD status input; ~~and~~

said communications system further comprising at least one third communications channel connecting said at least one LLD status output to said at least one PLD status input.

24. (previously presented) A communications system according to Claim 21 wherein:

said PLD further comprises a PLD receive interface including PLD parallel information inputs and at least one PLD control input; and

said LLD further comprising an LLD send interface including LLD parallel information outputs and at least one LLD control output;

said communications system further comprising fourth communications channels connecting said LLD information outputs to respective PLD information inputs, and at least one fifth communications channel connecting said at least one LLD control output with said at least one PLD control input so that said PLD and LLD are operable in a push-push configuration.

25. (previously presented) A communications system according to Claim 24 wherein:

said PLD send interface and said LLD send interface are mirrored; and

said PLD receive interface and said LLD receive interface are mirrored to thereby define symmetrical interfaces.

26. (previously presented) A communications system according to Claim 25 wherein:

said PLD receive interface further includes at least one PLD status output; and

said LLD send interface further includes at least one LLD status input; ~~and~~

said communications system further comprising at least one sixth communications channel connecting said at least one PLD status output to said at least one LLD status input.

27. (previously presented) A communications system according to Claim 21 wherein said LLD comprises:

an asynchronous transfer mode (ATM) device.

28. (previously presented) A communications system according to Claim 21 wherein said PLD comprises:

one of a synchronous optical network (SONET) device and a synchronous digital hierarchy (SDH) device.

29. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD; and

sending control signals over at least one second communications channel different from said first parallel communications channels from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said first parallel communications channels are provided over at least one electrical conductor.

30. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD, said sending information signals over said first parallel communications channels comprising:

operating a PLD send interface including PLD parallel information outputs, and

operating an LLD receive interface including LLD parallel information inputs; and

sending control signals over at least one second communications channel different from said first parallel communications channels from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals.

31. (previously presented) A method according to Claim 30 wherein said step of sending control signals over at least one second communications channel comprises:

operating a PLD send interface including at least one PLD control output; and

operating an LLD receive interface including at least one LLD control input.

32. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD;

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals; and

sending status signals over at least one third communications channel from said LLD to said PLD.

33. (previously presented) A method according to Claim 32 wherein said step of sending status signals over at least one third communications channel comprises:

operating a PLD send interface including at least one PLD status input; and

operating an LLD receive interface including at least one LLD status output.

34. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD;

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

sending information signals over third parallel communications channels from the LLD to the PLD; and

while sending control signals over at least one fourth communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals.

35. (previously presented) A method according to Claim 34 wherein said step of sending information signals over third parallel communications channels comprises:

operating an LLD send interface including LLD parallel information outputs; and

operating a PLD receive interface including PLD parallel information inputs.

36. (currently amended) A method according to Claim 35 wherein said step of sending control signals over at least one fourth communications channel comprises:

operating [[an]] said LLD send interface including at least one LLD control output; and

operating [[a]] said PLD receive interface including at least one PLD control input.

37. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD;

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals; and

sending status signals over at least one third communications channel from the PLD to the LLD.

38. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD;

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals; and

operating said PLD and LLD in a push-push configuration.

39. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD; and

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said PLD comprises a PLD send interface and said LLD comprises an LLD send interface mirrored to said PLD send interface; and

said PLD comprises a PLD receive interface and said LLD comprises an LLD receive interface mirrored to said PLD receive interface thereby define symmetrical interfaces.

40. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD; and

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said LLD comprises an asynchronous transfer mode (ATM) device.

41. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD; and

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

wherein said PLD comprises one of a synchronous optical network (SONET) device and a synchronous digital hierarchy (SDH) device.

42. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD;

sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

determining and appending a string-based framing code to each information symbol string of information symbol strings at said PLD to be transmitted in parallel over respective said first parallel communications channels, each string-based framing code being based upon at least some of said information symbols in said respective information symbol string; and

deskewing received information symbol strings at said LLD by aligning received parallel information symbol strings based upon said string-based framing codes.

43. (previously presented) A method according to Claim 42 wherein:

each information symbol comprises a binary bit; and

said step of determining and appending comprises determining and appending cyclic redundancy checking (CRC) codes to respective information bit strings.

44. (previously presented) A method according to Claim 43 wherein said step of deskewing comprises:

framing said information bit strings based upon said CRC codes.

45. (previously presented) A method according to Claim 39 wherein said step of deskewing comprises:

framing information symbol strings based upon respective string-based framing codes; and

aligning framed information symbol strings relative to one another and based upon said string-based framing codes.

46. (previously presented) A method according to Claim 45 wherein:

each information symbol comprises a binary bit;

said step of aligning comprising:

buffering framed information bits in at least one first-in-first-out (FIFO) device; and

aligning framed information bit strings during at least one of a writing and a reading phase of said at least one FIFO device and based upon said string-based framing codes.

47. (canceled)

48. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD, and while sending control signals over at least one second communications channel from [the] said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

determining and appending a string-based framing code to each information symbol string of information symbol strings at said PLD to be transmitted in parallel over respective said first parallel communications channels, each string-based framing code being based upon at least some of said information symbols in said respective information symbol string; and

deskewing received information symbol strings at said LLD by aligning received parallel information symbol strings based upon said string-based framing codes.

49. (previously presented) A method according to Claim 48 wherein:

each information symbol comprises a binary bit;

said step of determining and appending comprising determining and appending cyclic redundancy checking (CRC) codes to respective information bit strings.

50. (previously presented) A method according to Claim 49 wherein said step of deskewing comprises:

framing said information bit strings based upon said CRC codes.

51. (previously presented) A method according to Claim 48 wherein said step of deskewing comprises:

framing information bit strings based upon said respective string-based framing codes; and

aligning framed information bit strings relative to one another and based upon said string-based framing codes.

52. (previously presented) A method according to Claim 51 wherein:

each information symbol comprises a binary bit;

said step of aligning comprising:

buffering framed information bits in at least one first-in-first-out (FIFO) device; and

aligning framed information bit strings during at least one of a writing and a reading phase of said at least one FIFO device and based upon said string-based framing codes.

53. (previously presented) A method according to Claim 48 wherein said steps of sending information signals over first parallel communications channels comprises:

operating a PLD send interface including PLD parallel information outputs; and

operating an LLD receiver interface including LLD parallel information inputs.

54. (previously presented) A method according to Claim 48 wherein said step of sending control signals over at least one second communications channel comprises:

operating a PLD send interface including at least one PLD control output; and

operating an LLD receive interface including at least one LLD control input.

55. (previously presented) A method according to Claim 48 further comprising:

sending status signals over at least one third communications channel from said LLD to said PLD.

56. (previously presented) A method according to Claim 55 wherein said step of sending status signals over said at least one third communications channel comprises:

operating a PLD send interface including at least one PLD status input; and

operating an LLD receive interface including at least one LLD status input.

57. (previously presented) A method for communicating between a physical layer device (PLD) and a logical link device (LLD), the method comprising:

sending information signals over first parallel communications channels from said PLD to said LLD, and while sending control signals over at least one second communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals;

determining and appending a string-based framing code to each information symbol string of information symbol strings at said PLD to be transmitted in parallel over respective first parallel communications channels, each string-based framing code being based upon at least some of said information symbols in said respective information symbol string;

deskewing received information symbol strings at said LLD by aligning received parallel information symbol strings based upon said string-based framing codes;

sending information signals over third parallel communications channels from said LLD to said PLD; and

while sending control signals over at least one fourth communications channel from said PLD to said LLD so that control signals are sent from said PLD to said LLD out-of-band from information signals.

58. (previously presented) A method according to Claim 57 wherein said step of sending information signals over third parallel communications channels comprises:

operating an LLD send interface including LLD parallel information outputs; and

operating a PLD receive interface including PLD parallel information inputs.

59. (previously presented) A method according to Claim 58 wherein said step of sending control signals over at least one fourth communications channel comprises:

operating an LLD send interface including at least one LLD control output; and

operating a PLD receive interface including at least one PLD control input.

60. (previously presented) A method according to Claim 59 further comprising:

sending status signals over at least one fifth communications channel from said PLD to said LLD.

61. (previously presented) A method according to Claim 48 wherein said LLD comprises:

an synchronous transfer mode (ATM) device.

62. (previously presented) A method according to Claim 48 wherein said PLD comprises:

one of a synchronous digital network (SONET) device and a synchronous digital hierarchy (SDH) device.

63. (previously presented) A method according to Claim 48 wherein:

said first parallel communications channels are provided over at least one electrical conductor.